

Report on SSIP 2.0 Sensitization Program

Organized by SSIP Cell, Government Engineering College, Patan

Date: 31st July 2025 | Time: 11:30 am to 12:30 pm

Venue: Electronics and Communication Engineering Department, GEC Patan

The SSIP Cell of Government Engineering College, Patan organized an SSIP 2.0 Sensitization Program exclusively for the students of the Electronics and Communication Engineering Department on **31st July 2025**. The primary aim of this initiative was to create awareness about the Student Startup and Innovation Policy (SSIP) 2.0 and to motivate students to develop innovative solutions to real-world problems through PoC (Proof of Concept) and IPR (Intellectual Property Rights) frameworks.

The session was conducted by **Dr. Hitesh Panchal**, SSIP Coordinator of GEC Patan, who has been actively guiding and mentoring student innovators under the SSIP initiative. The event commenced at **11:30 am** and continued till **12:30 pm**, during which more than **90 students** from the Electronics & Communication department enthusiastically participated.

Dr. Panchal delivered an insightful session, beginning with a brief history and evolution of SSIP 1.0 and its successful transition into **SSIP 2.0**, which has a broader scope, increased funding support, and an enhanced focus on pre-incubation, IP generation, and prototype development. He emphasized the importance of innovation and creativity in engineering education, highlighting how SSIP empowers students with the opportunity to convert their ideas into working prototypes and patentable products.

The session covered the following key areas:

- **Overview of SSIP 2.0** and its key objectives.
- **Eligibility criteria** for student participation and idea submission.
- **Step-by-step guidance** on preparing and presenting a Proof of Concept (PoC).
- Importance of **design thinking, problem identification, and user-centric innovation**.
- Insights on **IPR processes**, including patent filing, copyright, and design registration.

- Showcasing **success stories** of previous SSIP-supported projects from GEC Patan and other institutions.
- Discussion on **funding support** structure under SSIP 2.0 and the role of mentors.

He also motivated students to take part in the upcoming PoC Presentation and Idea Competition events, assuring full support from the SSIP Cell for mentoring, documentation, and financial disbursement.

The session was interactive, with students raising pertinent queries about their ideas, funding procedures, timelines for PoC approval, and how to build teams. Dr. Panchal responded to each question with detailed explanations and encouraged students to think beyond traditional academic boundaries.

The program concluded with a vote of thanks delivered by the Head of the Department, who appreciated Dr. Panchal's efforts and encouraged the students to actively participate in SSIP activities. The session was highly impactful and served its purpose of sensitizing young innovators toward structured innovation practices.

Overall, the event was a significant step in strengthening the innovation ecosystem at GEC Patan and aligning student efforts with the goals of SSIP 2.0. It laid the foundation for increased participation from the Electronics and Communication Engineering students in future innovation and startup initiatives.

Pictures during the sensitization program:





Prepared by:

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GEC Patan